

## OrCAD Capture 17.2-2016 Advanced Annotation 功能介紹

Cadence<sup>®</sup> OrCAD<sup>®</sup> Capture 17.2 - 2016 提升 Annotate 零件序號編 輯功能,新版 Advanced Annotation 功能可對所有頁面零件自動編 排序號外,也可對單獨頁面或依照零件設立群組後做零件序號編排 功能,並且可以設定起始序號與結束序號,提升了電路圖零件序號 編排的方便性。



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- Revision :

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- · Version: Cadence OrCAD V17.2-2016 及以後版本
- ・備註:

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# **OrCAD Capture – Advanced Annotation**

OrCAD<sup>®</sup> Capture Annotate 功能可以讓使用者針對電路圖的零件做序號的編排,而 Advanced Annotation 功能,除了可以進行零件序號編排外,還可以針對電路圖頁面中,不同類型的零件 Reference Prefix 設定不同的零件序號範圍;並可依照零件設立的屬性作為群組條件做零件序號的編排。

## 功能操作說明:

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1. 於進入 OrCAD Capture/CIS, 開啟電路圖檔, 並且在專案管理視窗中, 選擇 Tools >> Annotate 功 能,在 Annotate 功能 UI 視窗出現後,點選 Advanced Annotation 功能。

Annotate		La-
Packaging PCB Editor Reuse Layout Reu	ISC	
<ul> <li>Refdes control required</li> <li>Scope</li> <li>Update entire design</li> <li>Update selection</li> </ul>		ser
Action Incremental reference update Inconditional reference update Reset part references to "?" Add Intersheet References Delete Intersheet References		613
Mode © Update Occurrences (Preferred) © Update Instances	Annotation Type Default	Gree
Physical Packaging Combined property string:	Additionally From INI :	Grase
<ul> <li>Reset reference numbers to begin at 1 in</li> <li>Annotate as per PM page ordering</li> <li>Do not change the page number</li> <li>Include non-primitive parts</li> </ul>	n each page ◎ Annotate as per page ordering in the title <u>b</u> locks	aser
<ul> <li>Preserve designator</li> <li>Preserve User Assigned Valid References</li> <li>Auto-package Heterogeneous Part Using</li> </ul>	s Advanced Annotation	er
	t推定 取済	e e



2. 進入 Advanced Annotation 功能視窗後,顯示電路圖的階層關係(依平坦式電路或階層式電路架構 顯示),選擇欲編輯頁面,與透過右手邊的功能,即可操作設定零件序號定義。

Design Hierarchy     O Property Block		Reference Range fo	r REFERENCE1	.DSN	
	Prefix	Instance Count	Start	End	
Design Hierarchy	TION	instance obtain	otart	End	
🛐 🥅 R00T-1					
🛐 🥅 R00T-2					
📥 🙀 🥅 ROOT-3					
다 주 🛄 DATA					
E 🔯 🥅 DATA					
E	Auto Fil	Prefix Add Row C	Delete Row(s) Dele	ete All Apply	
		Inherit	ed Ranges		
	Prefix	Start	1	End	
HIGH SPEED RAM					
RAM-1					
RAM-2					
	Action	Uncone	dition a l'un fono a con da		
			utional reference upda	te	
	Annotation Type	Default	:	te	
	Annotation Type Annotation Scheme	Default	te as per PM page ord	lering	
	Annotation Type Annotation Scheme Combined property string	Default Annota {Value	: ite as per PM page ord }{Source Package}{PO	lering WER_GROUP}	
	Annotation Type Annotation Scheme Combined property string Additionally from INI	Default Annota {Value	: : ite as per PM page ord }{Source Package}{PO	lering WER_GROUP}	
	Annotation Type Annotation Scheme Combined property string Additionally from INI Include non-primitive parts	Default Annotz (Value No	: : ite as per PM page ord }{Source Package}{PO	lering WER_GROUP}	
4	Annotation Type Annotation Scheme Combined property string Additionally from INI Include non-primitive parts Preserve designator	Default Annotz (Value No	itional reference uppa : ite as per PM page ord }{Source Package}{PO	lering WER_GROUP}	
Hierarchy View Show pages +	Annotation Type Annotation Scheme Combined property string Additionally from INI Include non-primitive parts Preserve designator	Default Annota (Value No No	itionaritereferice uppa : ite as per PM page ord }{Source Package}{PO	lering WER_GROUP}	

## 操作範例 1 (Design Hierarchy Annotate):

希望電路圖 ROOT-1 零件序號所有類別由 101、ROOT-2 由 201 開始編號,而其他下方階層式電路, 則不改變。

## 設定:

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將要設定的頁面打勾選取,點選右方的 Auto Fill Prefix 功能,可以對此頁面中,所有的零件 Prefix 類別自動新增,且在每項類別的 Prefix 後,依照可分開設定希望的零件起始與結束值,因為有提示每頁零件類型總共有多少,所以在編輯數量時,不必擔心設定的數值太多或是不足的狀況。最後下方的Action 功能,改變設定為「Unconditional reference update」(零件序號重新編輯),完成後點選Annotate 功能即可對只針對選取的線路圖頁面做編輯。





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## 操作範例 2 (Property Block Annotate):

電路圖依照階層式電路方塊不同,編輯不同的零件序號。

Oesign Hierarchy Property Block		Reference Range to	r REFERENCE1.DS	N	*
	Prefix	Instance Count	Start	End	1 265
Design Hierarchy					195
🛐 🥅 R00T-1					
😑 🙋 🔄 R00T-3					
	Auto Fill Pret	Add Row Dele	ete Row(s) Delete All	Apply	_
		Inherited	Ranges		_
	Prefix	Start	End	I	
S = RAM-1					
					_
	Action	Uncond	itional reference update		
	Annotation Type	Default			
	Annotation Scheme	Annotat	e as per PM page orderin	1	_
	Combined property string	(Value)	(Source Deckade) (DOW/EI		-
	comprised property string	\value}	10001 CE FACKAGE/(POWE)	_0\\00F/	= 68
	Additionally from INI				_
	Include non-primitive parts	No			_
•	Preserve designator	No			_
ierarchy View Show pages 🔻	Preserve user assigned valid refere	nces No			
			Annotata	Cancol	
			Annotate		

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## 操作設定:

首先在電路圖中,針對不同的階層式電路圖方塊中的零件,設定其群組屬性,如 ROOM,並且設定群 組名稱。DATA 方塊中的零件設定 ROOM=DATA、DAAMP 方塊設定 ROOM=DAAMP、 HIGH\_SPEED\_RAM 方塊設定 ROOM=RAM,設定好後重新啟動 Advanced Annotation。 選擇 Property Block 後,輸入設定的零件屬性名稱按下 Load 即可看見所設定的零件群組,再對每個 群組設定零件序號的編輯範圍後,按下 Annotate 進行序號編輯即可完成。

## 電路圖零件屬性定義:

	Part Reference	Value	Source Part	Source Library	Page	Schematic	ROOM
File 14, Hierarchy	C301	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
Design Resources	C302	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
	C303	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
B ROOT-1	C304	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
R00T-2	C305	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
R00T-3	C306	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
DAAMP	C307	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
DAAMP	C308	3300pf	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DATA	ROOT\DATA	DATA
🖻 🛱 DATA	C401	DG419	DG419_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DAAMP	ROOT\DATA/DAAMMP2	DAAMP
DATA	C402	.01UF	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DAAMP	ROOT\DATA/DAAMMP2	DAAMP
HIGH_SPEED_RAM	C403	DG419	DG419_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DAAMP	ROOT\DATA/DAAMMP1	DAAMP
RAM-1	C404	.01UF	CAP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	DAAMP	ROOT\DATA/DAAMMP1	DAAMP
Design Cache	C501	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-1	ROOT\HIGH_SPEED_RAM	RAM
Library	C502	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-1	ROOT\HIGH_SPEED_RAM	RAM
Outputs	C503	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-1	ROOT\HIGH_SPEED_RAM	RAM
Referenced Projects	C504	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-1	ROOT\HIGH_SPEED_RAM	RAM
	C505	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-2	ROOT\HIGH_SPEED_RAM	RAM
	C506	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-2	ROOT\HIGH_SPEED_RAM	RAM
	C507	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-2	ROOT\HIGH_SPEED_RAM	RAM
	C508	.1uf	CAP_NP_0	D:\TESTDSN\COMPARE_TEST\REFERENCE1.DSN	RAM-2	ROOT\HIGH_SPEED_RAM	RAM

#### Advanced Annotation 屬性定義:

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Advanced Annotation					×
💿 Design Hierarchy 🍥 Property Block		Reference Range	e for ROOM = DAT	Α	
	Prefix	Instance Count	Start	End	
Property Block	с	8	301	308	
ROOM Load	DAAMMP	2	301	302	
	R	9	301	309	
	U	2	301	302	
		-			
ROOM=RAM					
	Auto	Add Row	Delete Row(s) Delet		
	25	• 1			



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